



DEFENSE LOGISTICS AGENCY  
DLA LAND AND MARITIME  
POST OFFICE BOX 3990  
COLUMBUS, OH 43218-3990

IN REPLY  
REFER TO

DSCC-VQ (VQC-10-019895/Mr. Granchi/614-692-9975/pg)

December 15, 2010

SUBJECT: Full Q Level Certification and V Level Assembly for MIL-PRF-38535, FSC 5962

Mr. Larry Wallace  
General Manager  
Micross Components  
8701 Cross Park Drive  
Austin, TX 78754-4566

Dear Mr. Wallace:

Micross Components, formerly Austin Semiconductor, Inc. (ASI), has demonstrated to the Defense Supply Center, Columbus (DSCC) that it complies with MIL-PRF-38535, the performance specification used by the Department of Defense for monolithic integrated circuits.

Effective immediately, the previous Q Level Certification and V Level Assembly letter, DSCC-VQ (VQC-09-018811), granted to Austin Semiconductor, Inc. by DSCC is superseded by this certification to reflect the recent company name change and current certification status of Micross Components as approved by their Technical Review Board and documented in Micross' Quality Management Program.

The parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow Micross Components to mark parts with "Q" or "QML". This designator has been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., one which allows less government oversight), the use of world-wide commercial production lines, reduced finished product testing based on statistical process controls (SPC), and other cost advantages.

Testing must be performed using the facilities and methods listed in the letter DSCC-VQC-10-019894, Laboratory Suitability, or at facilities approved by the Micross Components Technical Review Board using its MIL-PRF-38535 Quality Management Program Plan.

This certification is subject to the conditions in DoD 4120.24-M, Defense Standardization Program and SD-6.

Any and all of the facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The manufacturer shall be responsible for all audit expenses incurred for the offshore facilities. Offshore facilities are subject to all of the conditions of MIL-PRF-38535.

QPL/QML manufacturers shall notify the qualifying activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their QPL/QML products. Failure to provide prior notification may be grounds for removal from QML-38535.

Finally, it is requested that the following activities be reported promptly to DSCC:

- Changes to certified facilities, process flows, or approved testing subcontractors
- Problem evaluation and a corrective action when:
  - a. A Technology Conformance Inspection (TCI) failure has been validated
  - b. The reliability of shipped parts is questionable.
- Test optimization, including:
  - a. Implementation - paragraph J.3.12, Appendix J, MIL-PRF-38535
  - b. Changing, suspending or canceling a prior test optimization
- Additions or deletions of parts in the QML-38535
- Change of company QML contact or other key QML personnel
- Changes in test methods and conditions

This certification is valid until terminated by written notice from the qualifying activity. If warranted, it may be withdrawn by this center at any time.

If you have any questions please contact Mr. Paul Granchi at (614) 692-9975.

Sincerely,



fr JOSEPH GEMPERLINE  
Chief  
Sourcing and Qualification Division

ENCLOSURE

cc:

VQC (Michael Grammens)  
VQC (Scott Thomas)  
VQ (Alberta Pertruskevich)  
Micross Components (Glenn Frashure)

Visit us on the web at: [www.dscclia.mil/offices/VQ](http://www.dscclia.mil/offices/VQ)

Enclosure to VQC-10-019895

<u>OPERATION</u>	<u>FACILITY</u>	<u>TECHNOLOGY/OPERATION</u>	<u>LOCATION</u>
<b>Wafer Fabrication:</b>			
	Samsung	SRAM	Seoul, South Korea
	Cypress Semiconductor	SRAM	Bloomington, MN
<b>Assembly:</b>			
**	Micross Components	- Chip Carriers	Micross Components
		- Flatpacks	8701 Cross Park Dr.
		- DIPs	Austin, TX 78754
		- PGAs	
		- Metal Cans	
		- J-Lead Chip Carriers	
		- Quad Flatpacks	
	Millenium	- Chip Carriers	Bangkok, Thailand
	Microtech	- Flatpacks	
		- DIPs	
		- Zips	
<b>Test:</b>			
		As baselined by lab suitability information letter VQC-10-019894	

\*\* V & Q Level assembly capability